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CONFIRMATION NO. FIRST NAMED INVENTOR ATTORNEY DOCKET NO. APPLICATION NO. **FILING DATE** 2668 09/883,959 06/20/2001 Hidemasa Zama 210067US-2

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT, LOGIC OPERATION CIRCUIT, AND FLIP FLOP

Al	APPLN. TYPE SMALL ENTITY ISS		ISSUE FI	SSUE FEE PUBLICATION FEE		TOTAL FEE(S) DU	DATE DUE	
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KABUSHIKI KAISHA TOSHIBA

Kawasaki-Shi, JAPAN

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